

## **Multichannel sequential pulse accumulator with a two-group memory addressing**

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### **Abstract**

Based on the two-group RAM IC addressing principle, a multichannel (256 channels) sequential data accumulator applying KP537Pv14A RAM IC with one-bit memory ( $4K \times 1$ ) is proposed. The channel capacity is  $2^{16}-1$ , and the input pulse repetition frequency is up to 62 kHz. © 1998 MAHK Hayka/Interperiodica Publishing.

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